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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Keith R. Slavin

Examiner: Reba I Elmore

Patent No.: 8,112,578

Group Art Unit: 2189

Issue Date: February 7, 2012

Docket No: 2001-0660.00/US

Title: LOW POWER, HASH-CONTENT ADDRESSABLE MEMORY ARCHITECTURE

Commissioner for Patents
Office of Patent Publication
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Certificate

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of Correction

We are transmitting herewith the attached:


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Respectfully submitted,

Date: February 24, 2012

By: 
Russ Slifer
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CERTIFICATE UNDER 37 CFR § 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450, on this 24th day of February 2012.

Shellie Bailey

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